```
EEEEEEEEEEEEEEE    VVV        VVV   LLL
EEEEEEEEEEEEEEE    VVV        VVV   LLL
EEEEEEEEEEEEEEEE   VVV        VVV   LLL
EEE                VVV        VVV   LLL
EEE                VVV        VVV   LLL
EEE                VVV        VVV   LLL
EEE                VVV        VVV   LLL
EEE                VVV        VVV   LLL
EEEEEEEEEEEE       VVV        VVV   LLL
EEEEEEEEEEEE       VVV        VVV   LLL
EEEEEEEEEEEE       VVV        VVV   LLL
EEE                VVV        VVV   LLL
EEE                VVV       VVV    LLL
EEE                VVV       VVV    LLL
EEE                   VVV   VVV     LLL
EEE                   VVV   VVV     LLL
EEE                   VVV   VVV     LLL
EEEEEEEEEEEEEEEE         VVV        LLLLLLLLLLLLLLL
EEEEEEEEEEEEEEE          VVV        LLLLLLLLLLLLLLL
EEEEEEEEEEEEEEE          VVV        LLLLLLLLLLLLLLL
```

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```
EEEEEEEEEE  VV      VV  LL           IIIIII  BBBBBBBB   RRRRRRRR      AAAAAA    RRRRRRRR    YY      YY
EEEEEEEEEE  VV      VV  LL           IIIIII  BBBBBBBB   RRRRRRRR      AAAAAA    RRRRRRRR    YY      YY
EE          VV      VV  LL             II    BB     BB  RR     RR    AA    AA   RR     RR   YY      YY
EE          VV      VV  LL             II    BB     BB  RR     RR    AA    AA   RR     RR   YY      YY
EE          VV      VV  LL             II    BB     BB  RR     RR    AA    AA   RR     RR     YY  YY
EE          VV      VV  LL             II    BB     BB  RR     RR    AA    AA   RR     RR     YY  YY
EEEEEEEE    VV      VV  LL             II    BBBBBBBB   RRRRRRRR     AA    AA   RRRRRRRR        YY
EEEEEEEE    VV      VV  LL             II    BBBBBBBB   RRRRRRR      AA    AA   RRRRRRR         YY
EE          VV      VV  LL             II    BB     BB  RR  RR      AAAAAAAAAA  RR  RR          YY
EE          VV      VV  LL             II    BB     BB  RR  RR      AAAAAAAAAA  RR  RR          YY
EE           VV    VV   LL             II    BB     BB  RR    RR    AA      AA  RR    RR        YY
EE           VV    VV   LL             II    BB     BB  RR    RR    AA      AA  RR    RR        YY
EEEEEEEEEE    VV  VV    LLLLLLLLLL   IIIIII  BBBBBBBB   RR     RR   AA      AA  RR     RR       YY
EEEEEEEEEE    VV  VV    LLLLLLLLLL   IIIIII  BBBBBBBB   RR     RR   AA      AA  RR     RR       YY
```

```
BBBBBBBB      333333     222222
BBBBBBBB      333333     222222
BB     BB   33      33  22      22
BB     BB   33      33  22      22
BB     BB           33          22
BB     BB           33          22
BBBBBBBB            33          22
BBBBBBBB            33          22
BB     BB           33  22
BB     BB           33  22
BB     BB   33      33  22
BB     BB   33      33  22
BBBBBBBB      333333    2222222222
BBBBBBBB      333333    2222222222
```

```
%TITLE  'EVLIBRARY   Symbol Definition Library'
!MODULE EVLIBRARY (
                LANGUAGE (BLISS32),
                IDENT = 'V04-000'
                ) =
!BEGIN
!
```

```
!++
! FACILITY:    DECnet Event Logging (EVL)

! ABSTRACT:

!       Event Logging Library of Common Definitions

! ENVIRONMENT: VAX/VMS Operating System

! AUTHOR:      Darrell Duffy   , CREATION DATE: 15-June-1980

! MODIFIED BY:

!      V001   TMH0001       Tim Halvorsen   25-Jun-1981
!             Remove some obsolete definitions
!--
```

```
%SBTTL  'Definitions'

!
! Structure declarations used for system defined structures to
! save typing.  These structures are byte sized.
!       (Thanks to A. Goldstein)
!

STRUCTURE
        BBLOCK [O, P, S, E; N] =
            [N]
            (BBLOCK+O)<P,S,E>,

        BBLOCKVECTOR [I, O, P, S, E; N, BS] =
            [N*BS]
            ((BBLOCKVECTOR+I*BS)+O)<P,S,E>
        ;


        Macro to create a bit id value for net control qio macros

        $BITID
        (
        Component prefix LNI, NDI, OBI, DLI, ...
        Type of parameter V, L, S
        Identifier for bit
        )

MACRO
        $BITID (COMP, TYP, ID) =
        (
        ( %NAME (COMP, '$C_', TYP, '_MASK') ) ^16 +
        ( $BITPOSITION (%NAME (COMP, '$V_', TYP, '_', ID) ) ) )
        )
        %;
```

```
%SBTTL  'Equated Symbols'

!
! EQUATED SYMBOLS:
!

LITERAL
        TRUE            = 1,
        FALSE           = 0,
        SUCCESS         = 1,
        FAILURE         = 0;

!END
!ELUDOM
```

EVLDEF
MDL

LIBTAIL
B32

XXXQUE
LIS

UTILDEF
REQ

ERRDEF
MDL

EVCDEF
MDL

CONSOLE
LIS

USEROPEN
LIS

VAXPSL
LIS

ERRFMT

VAX780REG
LIS

ERRFMT
MAP

EVLIBRARY
B32

VAX750REG
LIS

VECMAPREG
LIS

ERRFMT
LIS

EVL

LIBHEAD
B32

EVL
MAP

WQDEF
MDL